## PCB Package Changes

* Use standard pinout for DIN5 connector (pins are 1, 4, 2, 5, 3 NOT 1, 2, 3, 4, 5)
* Fix U1 pinout see datasheet for proper pinout.

## PCB Layout Changes

* Move DIN5 and PWR connectors closer to board edge
* Better pad length for U3
* Wider power/ground tracks
* Move a decoupling cap close to U2
* Smaller board size 8x10 vs 10x10

## Circuit Changes

* Move DAC to PORTB, connect ICP to ADC0
* Add a TX\_EN pin.
* Replace R24, R25 with pot. Set R23 to 10k

## Silk Screen Changes

* Move name/copyright to be horizontal
* Add a line for model number, revision, serial number
* Mark + side of polarized capacitors
* Mark pin 1 of JTAG connector and/or where the shroud slot goes